TO2T2T Th5T200T

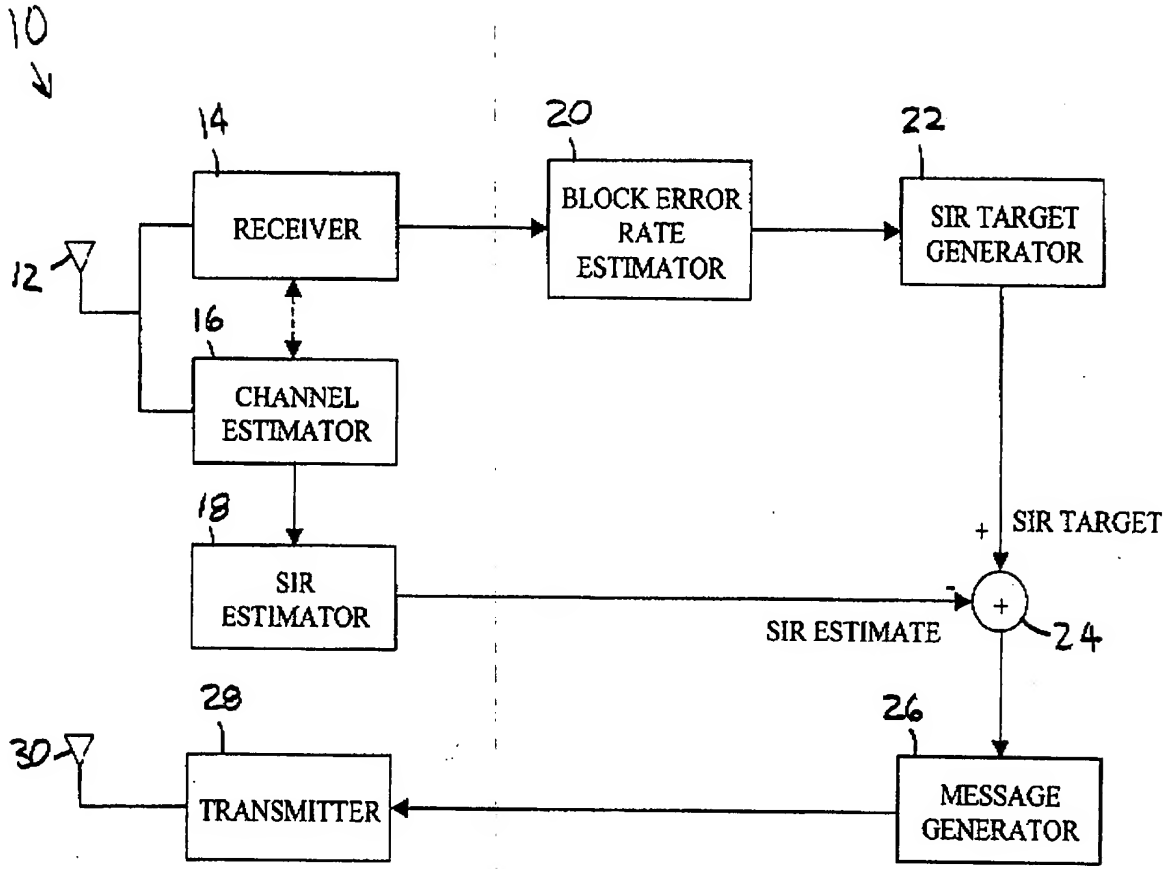

Fig. 1 (Prior Art)

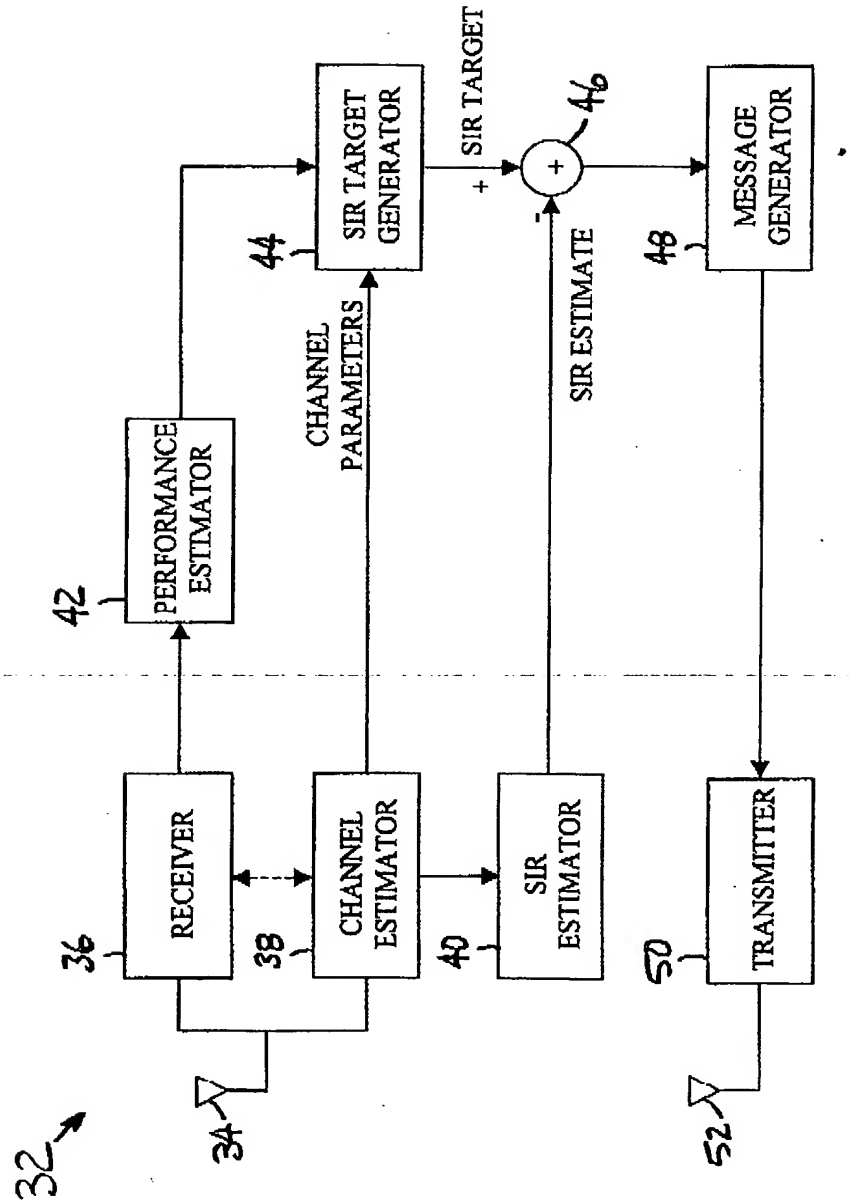
32
34
36
RECEIVER
38
CHANNEL ESTIMATOR
40
SIR ESTIMATOR
42
PERFORMANCE ESTIMATOR
44
SIR TARGET GENERATOR
46
+
-
SIR TARGET
SIR ESTIMATE
CHANNEL PARAMETERS
48
MESSAGE GENERATOR
50
TRANSMITTER
52

Fig. 2

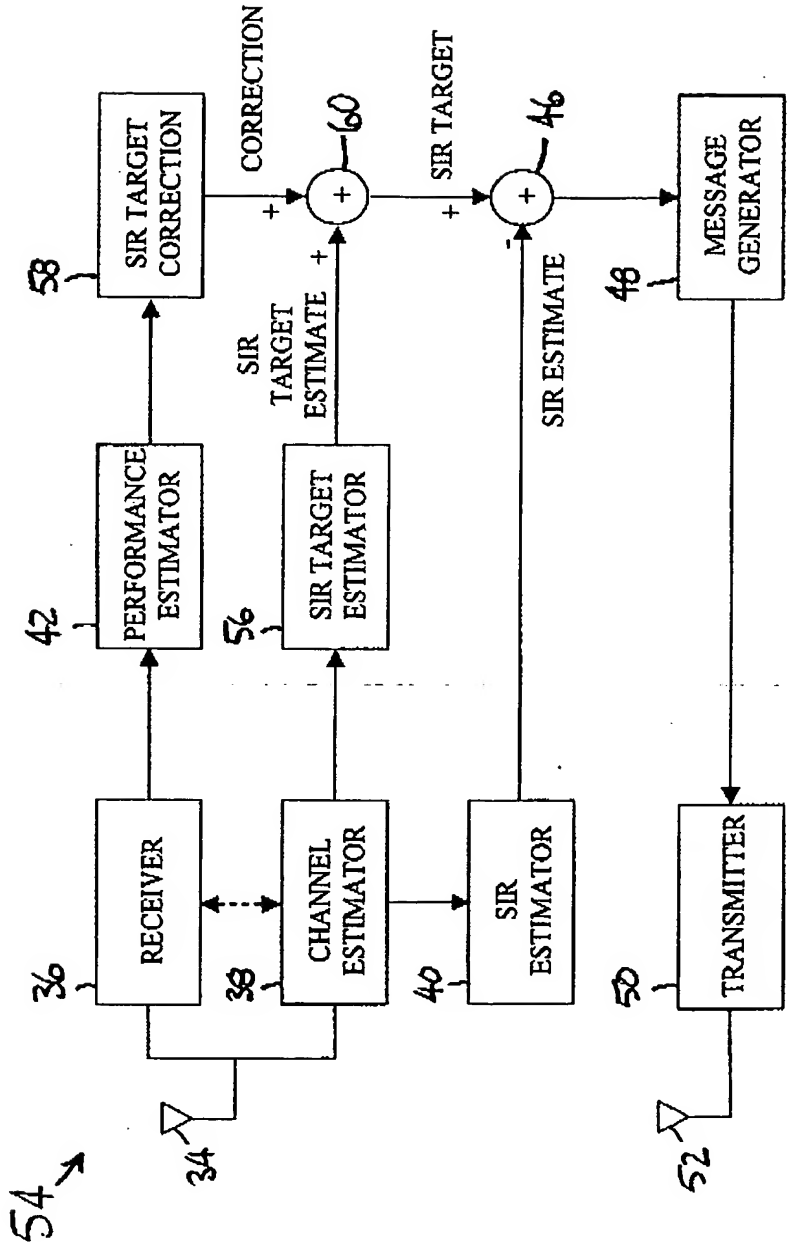
54
34
RECEIVER
36
CHANNEL ESTIMATOR
38
SIR ESTIMATOR
40
TRANSMITTER
50
52
PERFORMANCE ESTIMATOR
42
SIR TARGET ESTIMATOR
56
SIR TARGET CORRECTION
58
MESSAGE GENERATOR
48
CORRECTION
60
SIR TARGET
46
SIR TARGET ESTIMATE
SIR ESTIMATE
+
+
+
-

Fig. 3

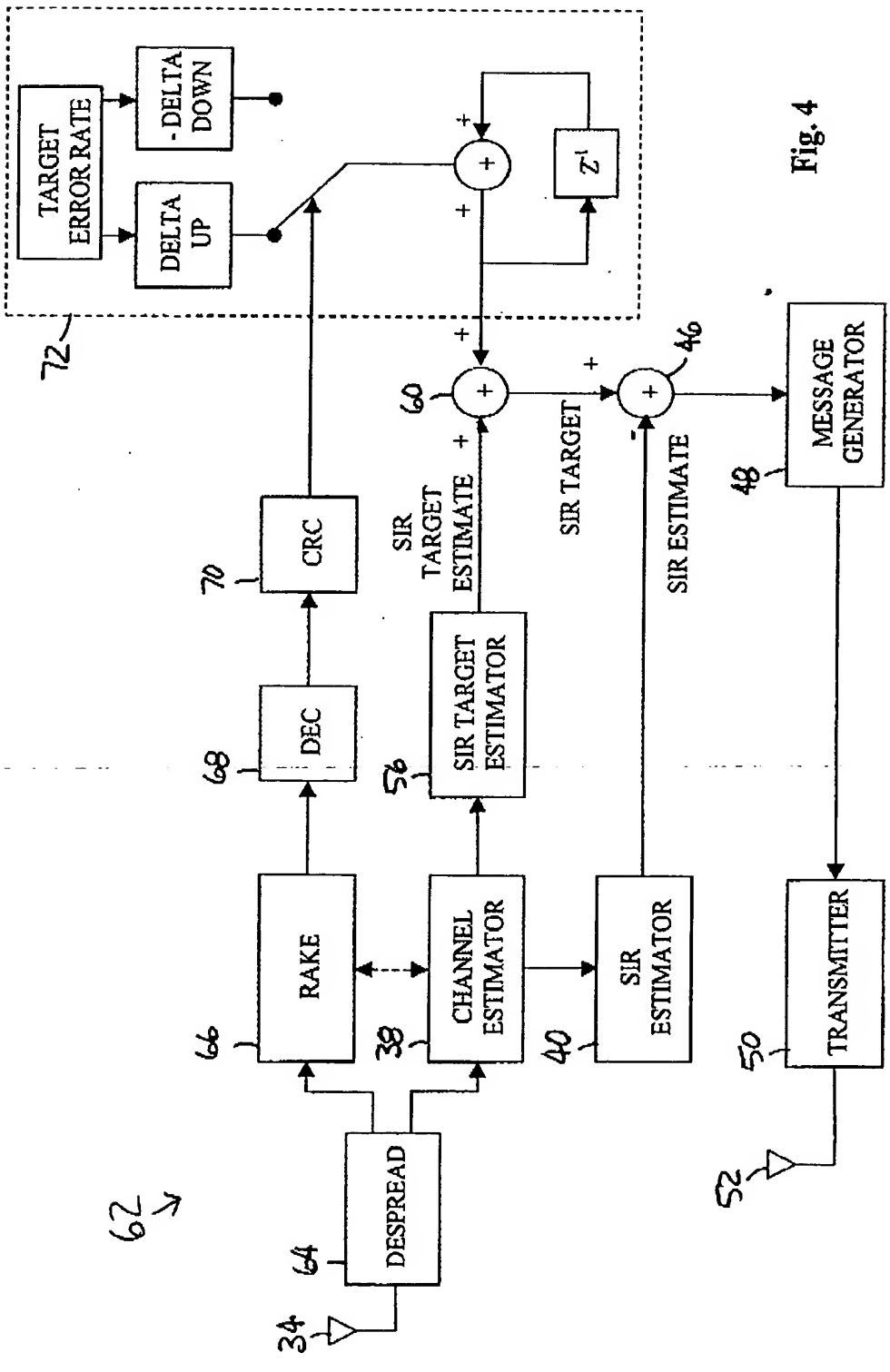
TARGET ERROR RATE
-DELTA DOWN
DELTA UP
72
+
+
+
Z^{-1}
60
+
+
46
SIR TARGET
SIR TARGET ESTIMATE
SIR ESTIMATE
MESSAGE GENERATOR
48
70
CRC
68
DEC
56
SIR TARGET ESTIMATOR
66
RAKE
38
CHANNEL ESTIMATOR
40
SIR ESTIMATOR
50
TRANSMITTER
52
62
64
DESPREAD
34

Fig. 4

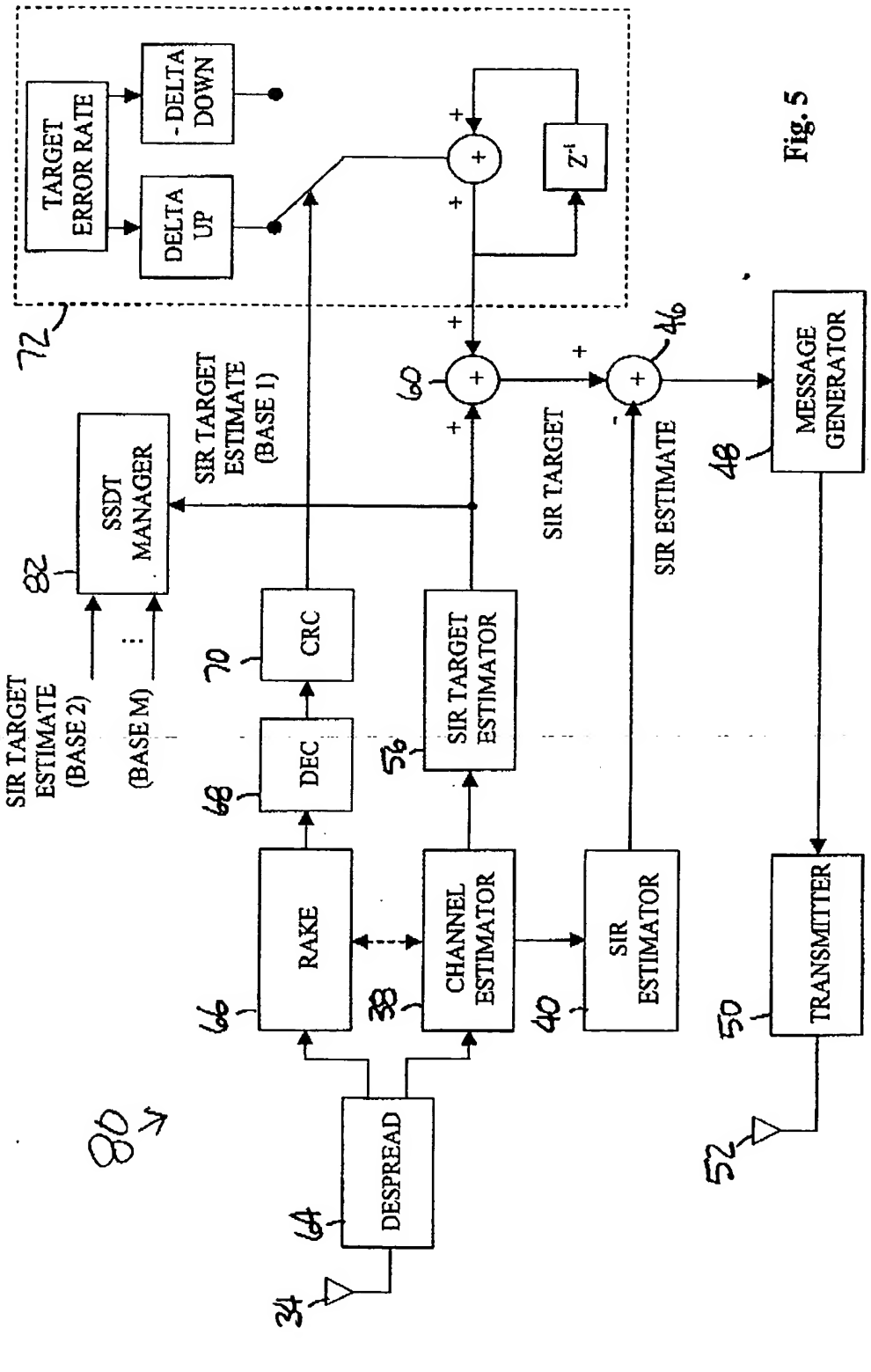
TARGET ERROR RATE
- DELTA DOWN
DELTA UP
Z-1
72
SSDT MANAGER
82
SIR TARGET ESTIMATE (BASE 1)
SIR TARGET ESTIMATE (BASE 2)
(BASE M)
CRC
70
DEC
68
SIR TARGET ESTIMATOR
56
60
SIR TARGET
SIR ESTIMATE
46
MESSAGE GENERATOR
48
RAKE
66
CHANNEL ESTIMATOR
38
SIR ESTIMATOR
40
TRANSMITTER
50
DESPREAD
64
34
52
80

Fig. 5